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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: To Be Assigned Examiner: To Be Assigned

Examiner. To be

In Re PATENT APPLICATION Of:

Applicants : Kazuhide ABE)
Serial No. : To Be Assigned)

Filed : December 31, 2003) INFORMATION) DISCLOSURE) STATEMENT

For : METHOD OF MANUFACTURING

SEMICONDUCTOR DEVICE)

Attorney Ref. : OKI 394)

December 31, 2003

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

This is an information disclosure statement submitted in compliance with the timing requirements of 37 C.F.R. §1.97(b)(1).

Attached are copies of four Japanese patent publications. Any relevance of the Japanese patent publications can be gleaned from the attached English-language Abstracts. The patent publications are listed on the attached Form PTO-1449. Also listed on the Form PTO-1449 for consideration are four U.S. patents. Any relevance of these U.S. patents is self-evident.

Since this Information Disclosure Statement is being filed with the application, no certification or fee is required, and the requirements of 37 C.F.R. §§1.97 and 1.98 are deemed to be fully met as to the documents submitted. Consideration of the submitted and listed documents is respectfully requested.

Respectfully submitted,

December 31, 2003

Date

Robert H. Berdo, Jr. (Reg. No. 38,075)

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FORM PTO-1449 INFORMATION DISCLOSURE STATEMENT				Atty Docket Applica		ation No.	
				OKI 394	To Be	To Be Assigned	
				Applicant Kazuhide ABE			
				Filing Date	Group	Group Unit	
				December 31, 2003		To Be Assigned	
			U.S.	PATENT DOCUMENTS	12020	11001811	
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	AO		 _				
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